

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,701,059

Docket No: 1997-0043.00/US

Issue Date: April 20, 2010

Patentee: Jigish D. Trivedi

Title: LOW RESISTANCE METAL SILICIDE LOCAL INTERCONNECTS AND A
METHOD OF MAKING



REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Pursuant to 1.20(a), the examiner is authorized to charge the Certificate of Correction fee of \$100.00 to the Deposit Account No. 13-3092.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

Customer Number: 26809

A handwritten signature in black ink, appearing to read "Russ Slifer".

Date: May 3, 2010

By: _____
Russ Slifer
Reg. No. 39,838

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 3 day of May 2010

Jenny Mundt

Name

A handwritten signature in black ink, appearing to read "Jenny Mundt".

Signature

05/07/2010 HBLANCO 00000016 133092 7701059
01 FC:1811 100.00 DA

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,701,059

Page (1) of 1

DATED : April 20, 2010

INVENTOR(S) : Jigish D. Trivedi

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 10, line 4, in Claim 2, delete "silicide," and insert - - silicide - -, therefor.

In column 10, line 38, in Claim 7, delete "said least one" and insert - - said at least one - -, therefor.

In column 10, line 55, in Claim 8, delete "transistor, and" and insert - - transistor; and - -, therefor.

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